

**Amendments to the Specification**

Please replace the paragraph beginning at line 19 on page 8 with the following amended paragraph.

Referring to Fig. 8, a conductive material 42 is formed over the substrate 34 and the sidewall spacers 36 and may completely fill the spaces 40a through 40i. An example material for the layer 42 ~~32~~ is conductively doped polysilicon.